Vishay High Power Products

ADD-A-PAK Generation VII Power Modules Schottky Rectifier, 220 A



- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- Low thermal resistance
- UL pending
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- Easy mounting on heatsink

ELECTRICAL DESCRIPTION

The VSKDS440.. Schottky rectifier doubler has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS SYMBOL **CHARACTERISTICS** VALUES UNITS 220 I_{F(AV)} Rectangular waveform А 30 v V_{RRM} $t_{\rm p} = 5 \ \mu s \ sine$ 27 000 А I_{FSM} V_{F} 110 Apk, T_J = 125 °C 0.43 V °C ΤJ Range - 55 to 150

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VSKDS440/030	UNITS	
Maximum DC reverse voltage	V _R	30	V	
Maximum working peak reverse voltage	V _{RWM}		v	



The ADD-A-PAK generation VII, new generation of

ADD-A-PAK module, combines the excellent thermal

performances obtained by the usage of exposed direct

bonded copper substrate, with advanced compact simple

package solution and simplified internal structure with

220 A

PRODUCT SUMMARY

MECHANICAL DESCRIPTION

IF(AV)

minimized number of interfaces.





VSKDS440/030

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$I_{F(AV)}$ 50 % duty cycle at T _C = 97 °C, rectangular waveform		220	
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	27 000	А
	IFSM	10 ms sine or 6 ms rect. pulse		3000	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 20 \text{ A}, L = 1 \text{ mH}$ 19		198	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 44		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	220 A	- T _J = 25 °C	0.68	V
		440 A		1.0	
		220 A	- T _J = 125 °C	0.61	
		440 A		0.93	
Maximum reverse leakage current	I _{RM}	T _J = 25 °C	V _R = Rated V _R	20	mA
		T _J = 125 °C		1120	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		14 800	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs
Maximum RMS insulation voltage	V _{INS}	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.26	26 °C/W	
Typical thermal resistance, case to heatsink per module		R _{thCS}		0.1	0/11	
Approximate weight				75	g	
Approximate weight	e weight			2.7	oz.	
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the		Nm	
	busbar		spread of the compound.	3	INIII	
Case style			JEDEC	TO-240AA co	mpatible	



1000

100

10

1

0.0

0.2

0.4

Tj = 125°C

Ti = 25°C

0.6

Forward Voltage $Drop-V_{FM}(V)$

Fig. 1 - Maximum Forward Voltage Drop Characteristics

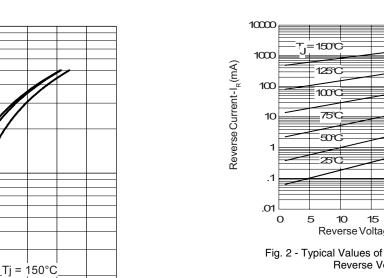
0.8

1.0 1.2

Instantaneous Forward Current-I_F(A)

VSKDS440/030

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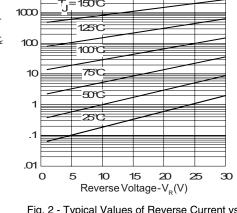
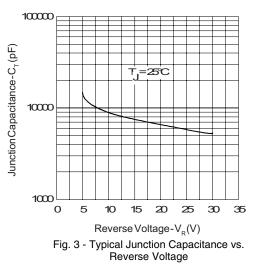
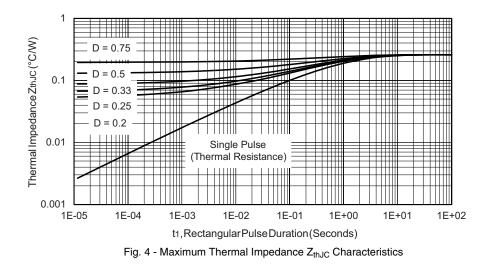


Fig. 2 - Typical Values of Reverse Current vs. **Reverse Voltage**



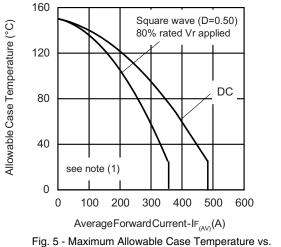


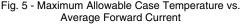
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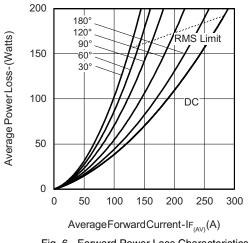
VSKDS440/030

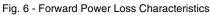
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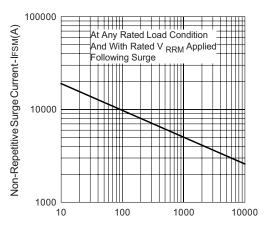
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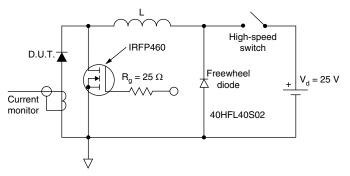








SquareWavePulseDuration-t_n(microsec) Fig. 7 - Maximum Non-Repetitive Surge Current





Note

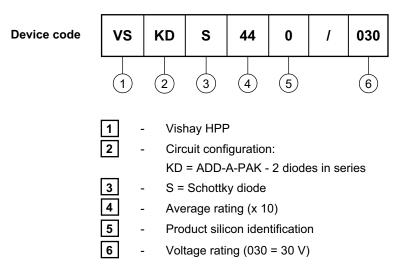
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R$

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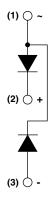


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ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			



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